



2811

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TRANSMITTAL FORM <small>(to be used for all correspondence after initial filing)</small>	Application Number	09/420,086
	Filing Date	10/18/1999
	First Named Inventor	WARREN M. FARNWORTH
	Art Unit	2811
	Examiner Name	PAREKH, N.
Total Number of Pages in This Submission	Attorney Docket Number	98-105.1

ENCLOSURES (Check all that apply)		
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<input type="checkbox"/> Response to Missing Parts/Incomplete Application	Remarks Enclosed for filing is an Amendment in response to the Office Action dated 12/19/2002, having a statutory period for response set to expire on 3/19/2003. Also enclosed is a return receipt postcard.	
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual	Stephen A. Gratton, #28,418 THE LAW OFFICE OF STEPHEN A. GRATTON
Signature	
Date	

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I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on this date: 3/13/2003	
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Amend
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3.27.03
CPS

In re application of:

WARREN M. FARNWORTH
ALAN G. WOOD

Serial No. 09/420,086

Art Unit: 2811

Filing Date: October 18, 1999

Examiner: PAREKH, N.

For: SEMICONDUCTOR COMPONENT HAVING
LASER MACHINED CONDUCTORS
(AS AMENDED)

Attorney Docket No. 98-0105.01

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AMENDMENT
March 13, 2003

Assistant Commissioner of Patents
BOX AMENDMENT (NON FEE)
Washington, D.C. 20231

Sir:

This Amendment is in response to the Office Action dated December 19, 2002, having a statutory period for response set to expire on March 19, 2003. Please amend the application as follows.

In the Claims

Please amend independent claims 25, 30, 35, 47 and 52. Following is a clean version of the amended claims, and a clean version of all the pending claims. In addition, a marked version showing the changes to the amended claims follows the Remarks section of this Amendment.

Clean Version Of Amended Claims

25. (five times amended) A semiconductor component comprising:
a substrate having a surface;

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